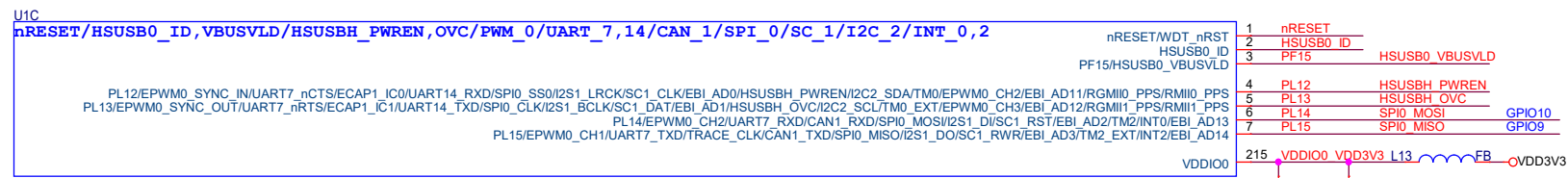
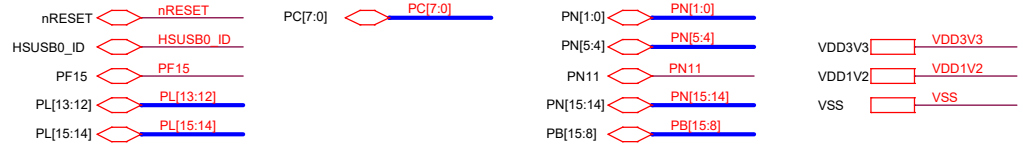
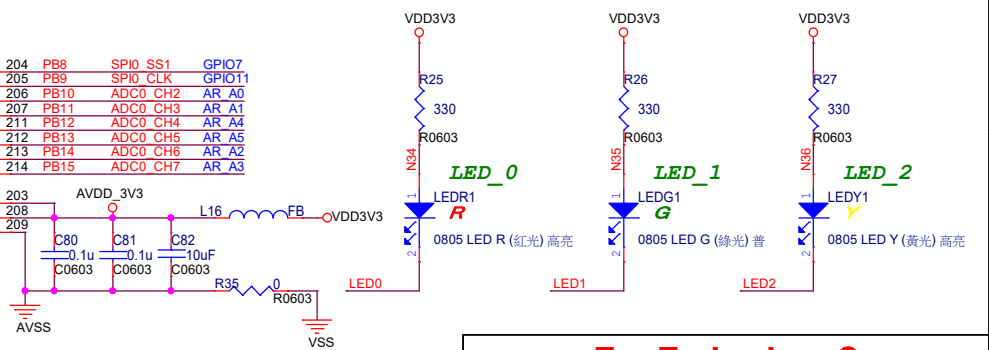
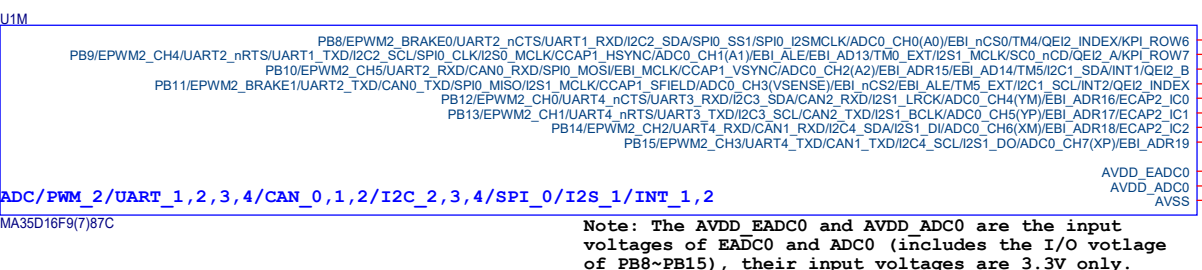
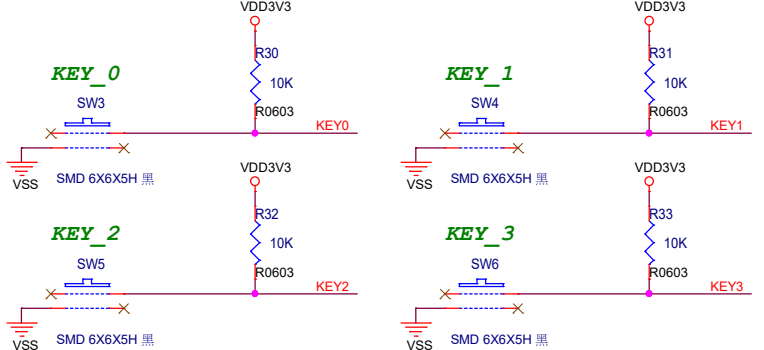
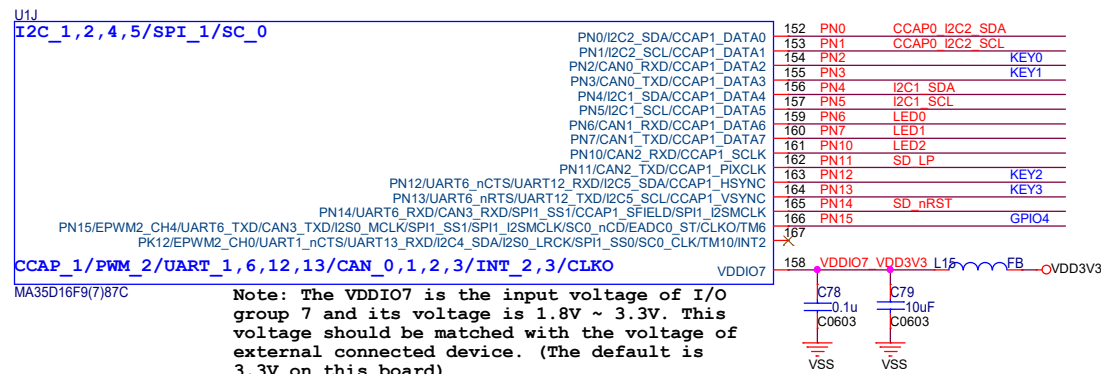
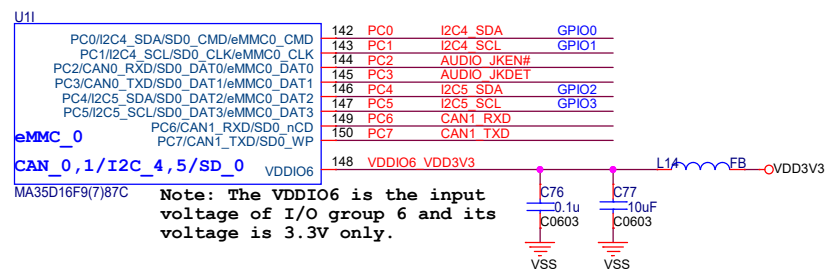
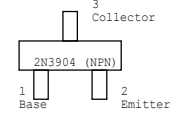
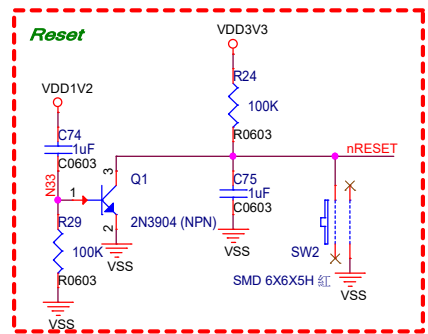


**Option for the supply voltage of SDRAM power (DRAM VDD)**

1. Mount R252 only, DRAM\_VDD = 1.35V (for MA35D16F887C/MA35D16F987C, DDR3L)
2. Mount R253 only, DRAM\_VDD = 1.8V (for MA35D16F787C, DDR2)



Note: The VDDIO0 is the input voltage of I/O group 0 and its voltage is 3.3V only.



nuvoTon Technology Corp.

Title  
NuMaker-IoT-MA35D16F90 (LQFP216)

Size B Document Number  
02. VDDIO0/6/7/ADC Rev V2.3

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U1D  
NAND/UART\_1,2,3,4,5,8,16

PA0/UART1\_nCTS/UART16\_RXD/NAND\_DATA0/EBI\_AD0/EBI\_AD0  
PA1/UART1\_nRTS/UART16\_TXD/NAND\_DATA1/EBI\_AD1/EBI\_ADR1  
PA2/UART1\_TXD/NAND\_DATA2/EBI\_AD2/EBI\_ADR2  
PA3/UART1\_RXD/NAND\_DATA3/EBI\_AD3/EBI\_ADR3  
PA4/UART3\_nCTS/UART2\_RXD/NAND\_DATA4/EBI\_AD4/EBI\_ADR4  
PA5/UART3\_nRTS/UART2\_TXD/NAND\_DATA5/EBI\_AD5/EBI\_ADR5  
PA6/UART3\_RXD/NAND\_DATA6/EBI\_AD6/EBI\_ADR6  
PA7/UART3\_TXD/NAND\_DATA7/EBI\_AD7/EBI\_ADR7  
PA8/UART5\_nCTS/UART4\_RXD/NAND\_RDY0/EBI\_AD8/EBI\_ADR8  
PA9/UART5\_nRTS/UART4\_TXD/NAND\_nRE/EBI\_AD9/EBI\_ADR9  
PA10/UART5\_RXD/NAND\_nWE/EBI\_AD10/EBI\_ADR10  
PA11/UART5\_TXD/NAND\_CLE/EBI\_AD11/EBI\_ADR11  
PA12/UART7\_nCTS/UART8\_RXD/NAND\_ALE/EBI\_AD12/EBI\_ADR12  
PA13/UART7\_nRTS/UART8\_TXD/NAND\_nCS0/EBI\_AD13/EBI\_ADR13  
PA14/UART7\_RXD/CAN3\_RXD/NAND\_nWP/EBI\_AD14/EBI\_ADR14

PowerOnSetting/TSI\_SWD  
PG0/EPWM0\_CH0/UART7\_TXD/CAN3\_TXD/SPI0\_SS0/EADC0\_ST/EBI\_AD15/I2S1\_MCLK/QEI0\_INDEX/TM1\_CLKO/INT0/EBI\_ADR15/PowerOnSetting  
INT\_0,1,2,3/UART\_5,6,9  
PG1/EPWM0\_CH3/UART9\_nRTS/UART6\_TXD/I2C4\_SCL/CAN2\_TXD/EBI\_nCS0/QEI0\_B/TM1\_EXT/RGMII1\_PPS/RMI1\_PPS/PowerOnSetting  
SC\_1/PWM\_0,1  
PG3/EPWM0\_CH5/UART9\_TXD/CAN0\_TXD/SPI0\_I2SMCLK/TSI\_SWD\_CLK/EBI\_ADR17/EBI\_nCS1/EBI\_MCLK/QEI0\_B/TM3\_EXT/I2S1\_MCLK/PowerOnSetting  
PG4/EPWM1\_CH0/UART5\_nCTS/UART6\_RXD/SPI3\_SS0/QEI1\_INDEX/EBI\_ADR18/EBI\_nCS0/I2S1\_DO/SC1\_CLK/TM4/TSI\_UART\_RXD/INT2/ECAP1\_IC2/PowerOnSetting  
PG5/EPWM1\_CH1/UART5\_nRTS/UART6\_TXD/SPI3\_CLK/ECAP0\_IC0/EBI\_ADR19/EBI\_ALE/I2S1\_DI/SC1\_DAT/TM4\_EXT/TSI\_UART\_TXD/PowerOnSetting  
SPI\_3/I2S\_1/CLKO/PWM\_0/CAN\_0,1,2  
PG6/EPWM1\_CH2/UART5\_RXD/CAN1\_RXD/SPI3\_MOSI/ECAP0\_IC1/EBI\_nRD/I2S1\_BCLK/SC1\_RST/TM7/INT3/PowerOnSetting  
PG7/EPWM1\_CH3/UART5\_TXD/CAN1\_TXD/SPI3\_MISO/ECAP0\_IC2/EBI\_nWR/I2S1\_LRCK/SC1\_PWR/TM7\_EXT/PowerOnSetting

JTAG/I2S\_0  
PG11/JTAG\_TDO/I2S0\_MCLK/NAND\_RDY1/EBI\_nWRH/EBI\_nCS1/EBI\_AD0  
PG12/JTAG\_TCK/SW\_CLK/I2S0\_LRCK/EBI\_nWRL/EBI\_AD1  
PG13/JTAG\_TMS/SW\_DIO/I2S0\_BCLK/EBI\_MCLK/EBI\_AD2  
PG14/JTAG\_TDI/I2S0\_DI/NAND\_nCS1/EBI\_ALE/EBI\_AD3  
PG15/JTAG\_nTRST/I2S0\_DO/EBI\_nCS0/EBI\_AD4

QSPI\_1/PWM\_0/USRT\_1,15,16  
PD6/EPWM0\_SYNC\_IN/UART1\_RXD/QSPI1\_MOSI/I2C0\_SDA/I2S0\_MCLK/EPWM0\_CH0/EBI\_AD5/SPI3\_SS1/TRACE\_CLK  
PD7/EPWM0\_SYNC\_OUT/UART1\_TXD/QSPI1\_MISO/I2C0\_SCL/I2S1\_MCLK/EPWM0\_CH1/EBI\_AD6/SC1\_nCD/EADC0\_ST  
PD8/EPWM0\_BRAKE0/UART16\_nCTS/UART15\_RXD/QSPI1\_SS0/I2S1\_LRCK/EPWM0\_CH2/EBI\_AD7/SC1\_CLK/TM0  
PD9/EPWM0\_BRAKE1/UART16\_nRTS/UART15\_TXD/QSPI1\_CLK/I2S1\_BCLK/EPWM0\_CH3/EBI\_AD8/SC1\_DAT/TM0\_EXT  
PD10/EPWM1\_BRAKE0/UART16\_RXD/QSPI1\_MOSI/I2S1\_DI/EPWM0\_CH4/EBI\_AD9/SC1\_RST/TM2  
PD11/EPWM1\_BRAKE1/UART16\_TXD/QSPI1\_MISO/I2S1\_DO/EPWM0\_CH5/EBI\_AD10/SC1\_PWR/TM2\_EXT

UART\_0  
PE14/UART0\_TXD  
PE15/UART0\_RXD

MA35D16F9(7)87C

24 PA0 NAND DATA0  
25 PA1 NAND DATA1  
26 PA2 NAND DATA2  
27 PA3 NAND DATA3  
28 PA4 NAND DATA4  
29 PA5 NAND DATA5  
30 PA6 NAND DATA6  
31 PA7 NAND DATA7  
32 PA8 NAND RDY  
33 PA9 NAND nRE  
34 PA10 NAND nWE  
35 PA11 NAND CLE  
36 PA12 NAND ALE  
37 PA13 NAND nCS  
39 PA14 NAND nWP

40 PG0 GPIO8  
41  
42 PG1  
43 PG2 AR D2  
44 PG3 AR D3  
45 PG4 GPIO21  
46 PG5 GPIO20  
47 PG6 GPIO18  
48 PG7 GPIO19

50 PG11  
51 PG12  
52 PG13  
53 PG14  
54 PG15

58 PD6 AR D4  
59 PD7 AR D5  
60 PD8 AR D6  
61 PD9 AR D7  
62 PD10 AR D8  
63 PD11 AR D9

56 PE14 UART0\_TXD  
57 PE15 UART0\_RXD

38 VDDIO1 VDDIO1  
64

PA[14:0] PA[14:0]  
PG0 PG0  
PG[7:2] PG[7:2]  
PG[15:11] PG[15:11]  
PD[11:6] PD[11:6]  
PE[15:14] PE[15:14]  
nRESET nRESET  
VDD3V3 VDD3V3  
VSS VSS

PG11~15 Connect to SWJ(I2S0)  
PG11 R41 0 (NC) JTAG TDO  
PG12 R42 0 (NC) JTAG TCK SW\_CLK  
PG13 R43 0 (NC) JTAG TMS SW\_DIO  
PG14 R44 0 (NC) JTAG TDI  
PG15 R45 0 (NC) JTAG nTRST

Power-on Setting

VDD3V3O  
R188  
0  
R0603  
N122  
R202 R40 R201 R39 R233 R38 R37 R36  
10K 10K 10K 10K 10K 10K 10K 10K  
R0603 R0603 R0603 R0603 R0603 R0603 R0603 R0603  
N37 N38 N39 N40 N41 N42 N43 N44  
SW7  
SW DIP 8 (SMD)  
Internal pull-down

PG0 Secure Boot  
L Secure Boot Enable  
H Secure Boot Disable

PG1 Boot Source QSPI0, SD/eMMC I/O Voltage  
L 3.3V  
H 1.8V

PG3 PG2 Boot Source  
L QSPI0 Flash  
L H SD/eMMC  
H L NAND Flash  
H H USB

PG7 PG6 Booting from QSPI0  
L L SPI-NAND, 1-bit  
H L SPI-NOR, 1-bit

PG6 Booting from SD/eMMC  
L SD0/eMMC0 booting  
H SD1/eMMC1 booting

PG7 Booting from SD/eMMC  
L eMMC 4-bit booting  
H eMMC 8-bit booting

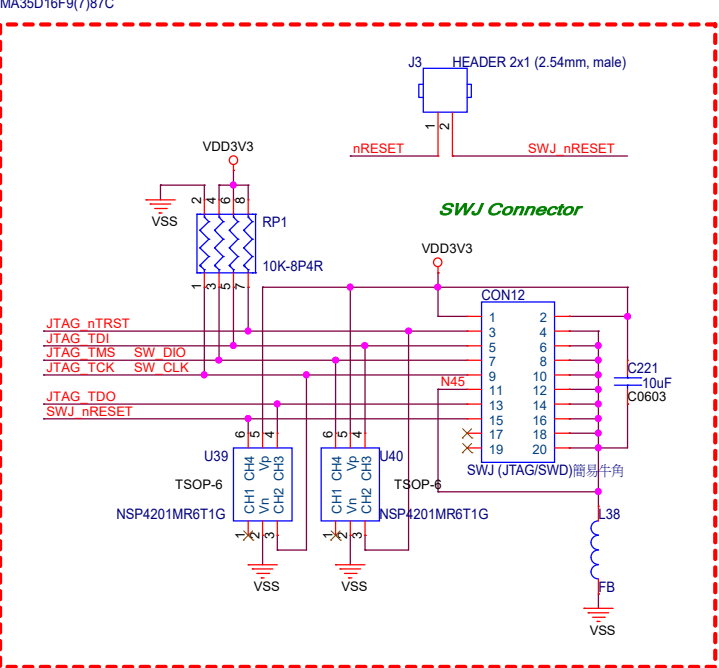
PG5 PG4 Booting from NAND  
L L Ignore  
L H NAND flash page 2KB  
H L NAND flash page 4KB  
H H NAND flash page 8KB

PG7 PG6 Booting from NAND  
L L Ignore  
L H BCH T12  
H L BCH T24  
H H NO ECC

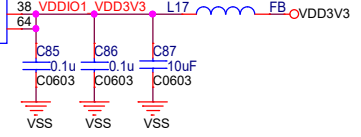
PG4 Booting from USB  
L USB0 booting  
H USBH booting

PG5 Booting from USBH  
L USBH port 0 booting  
H USBH port 1 booting

PG6 Booting from USBH  
L Over-current low-active detect  
H Over-current high-active detect



Note: The VDDIO1 is the input voltage of I/O group 1 and its voltage is 3.3V only.



U1E

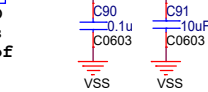
PK9/I2C3_SCL/CCAP0_SCLK/EBI_AD0/EBI_ADR0	65	PK9	CCAP0_SCLK
PK10/CAN1_RXD/CCAP0_PIXCLK/EBI_AD1/EBI_ADR1	66	PK10	CCAP0_PIXCLK
PK11/CAN1_TXD/CCAP0_HSYNC/EBI_AD2/EBI_ADR2	67	PK11	CCAP0_HSYNC
PM0/I2C4_SDA/CCAP0_VSYNC/EBI_AD3/EBI_ADR3	68	PM0	CCAP0_VSYNC
PM1/I2C4_SCL/SP13_I2SMCLK/CCAP0_SFIELD/EBI_AD4/EBI_ADR4	69	PM1	CCAP0_nRST
PM2/CAN3_RXD/CCAP0_DATA0/EBI_AD5/EBI_ADR5	70	PM2	CCAP0_DATA0
PM3/CAN3_TXD/CCAP0_DATA1/EBI_AD6/EBI_ADR6	71	PM3	CCAP0_DATA1
PM4/I2C5_SDA/CCAP0_DATA2/EBI_AD7/EBI_ADR7	72	PM4	CCAP0_DATA2
PM5/I2C5_SCL/CCAP0_DATA3/EBI_AD8/EBI_ADR8	73	PM5	CCAP0_DATA3
PM6/CAN0_RXD/CCAP0_DATA4/EBI_AD9/EBI_ADR9	74	PM6	CCAP0_DATA4
PM7/CAN0_TXD/CCAP0_DATA5/EBI_AD10/EBI_ADR10	75	PM7	CCAP0_DATA5
PM8/I2C0_SDA/CCAP0_DATA6/EBI_AD11/EBI_ADR11	76	PM8	CCAP0_DATA6
PM9/I2C0_SCL/CCAP0_DATA7/EBI_AD12/EBI_ADR12	77	PM9	CCAP0_DATA7
PM10/EPWM1_CH2/CAN2_RXD/SP13_SS0/CCAP0_DATA8/SP12_I2SMCLK/EBI_AD13/EBI_ADR13	78	PM10	CCAP0_DATA8
PM11/EPWM1_CH3/CAN2_TXD/SP13_SS1/CCAP0_DATA9/SP12_SS1/EBI_AD14/EBI_ADR14	79	PM11	CCAP0_DATA9
	80	PM11	CCAP0_DATA9

CCAP\_0/PWM\_1/CAN\_0,1,2,3/I2C\_0,3,4,5

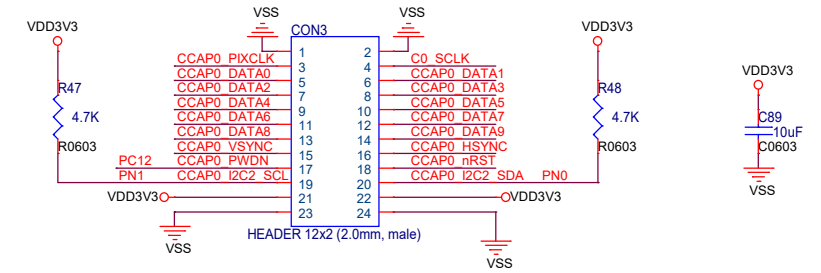
MA35D16F9(7)87C

Note: The VDDIO2 is the input voltage of I/O group 2 and its voltage is 1.8V ~ 3.3V. This voltage should be matched with the voltage of external connected device. (The default is 3.3V on this board)

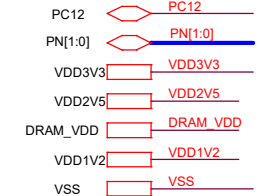
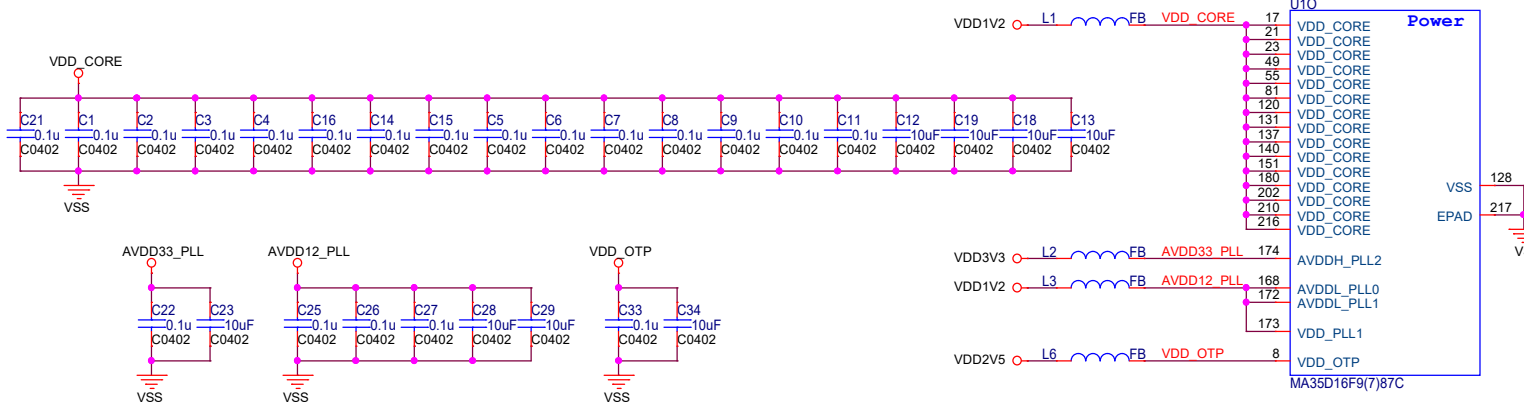
VDDIO2



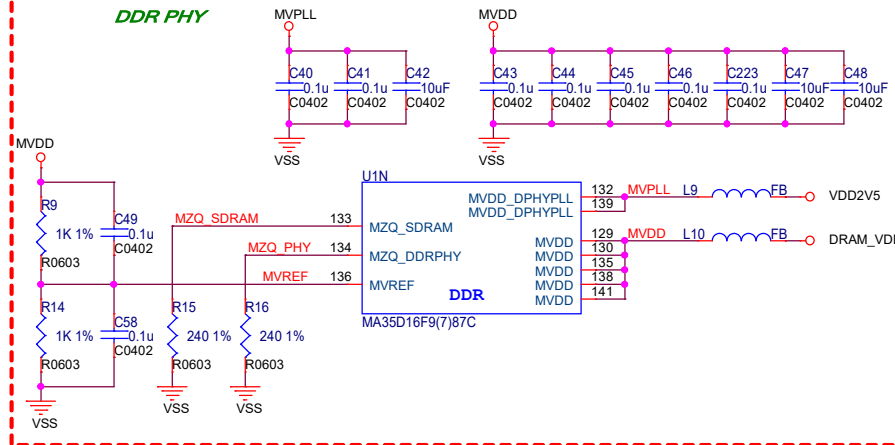
## CCAP0 Connector



### Power



### DDR PHY



nuvoTon Technology Corp.

Title  
NuMaker-IoT-MA35D16F90 (LQFP216)

Size B Document Number  
04. CCAP0 (VDDIO2) Rev V2.3

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U1G

PG8/EPWM1\_CH4/UART12\_RXD/CAN3\_RXD/SPI2\_SS0/LCM\_VSYNC/LCM\_MPU\_RD/EN/I2C3\_SDA/EBI\_AD7/EBI\_nCS0  
 PG9/EPWM1\_CH5/UART12\_TXD/CAN3\_TXD/SPI2\_CLK/LCM\_HSYNC/LCM\_MPU\_WR/RW/I2C3\_SCL/EBI\_AD8/EBI\_nCS1  
 PG10/UART12\_nRTS/UART13\_TXD/SPI2\_MOSI/LCM\_CLK/EBI\_AD9/EBI\_nWR  
 PK4/UART12\_nCTS/UART13\_RXD/SPI2\_MISO/LCM\_DEN/LCM\_MPU\_RS/EBI\_AD10/EBI\_nWRL  
 PH8/UART4\_nCTS/UART3\_RXD/LCM\_DATA0/LCM\_MPU\_D0/EBI\_AD11  
 PH9/UART4\_nRTS/UART3\_TXD/LCM\_DATA1/LCM\_MPU\_D1/EBI\_AD12  
 PH10/UART4\_RXD/LCM\_DATA2/LCM\_MPU\_D2/EBI\_AD13  
 PH11/UART4\_TXD/LCM\_DATA3/LCM\_MPU\_D3/EBI\_AD14  
 PH12/UART6\_nCTS/UART5\_RXD/LCM\_DATA4/LCM\_MPU\_D4  
 PH13/UART6\_nRTS/UART5\_TXD/LCM\_DATA5/LCM\_MPU\_D5  
 PH14/UART6\_RXD/LCM\_DATA6/LCM\_MPU\_D6  
 PH15/UART6\_TXD/LCM\_DATA7/LCM\_MPU\_D7  
 PH0/UART8\_nCTS/UART7\_RXD/LCM\_DATA8/LCM\_MPU\_D8  
 PH1/UART8\_nRTS/UART7\_TXD/LCM\_DATA9/LCM\_MPU\_D9  
 PH2/UART8\_RXD/LCM\_DATA10/LCM\_MPU\_D10  
 PH3/UART8\_TXD/LCM\_DATA11/LCM\_MPU\_D11  
 PH4/UART10\_nCTS/UART9\_RXD/LCM\_DATA12/LCM\_MPU\_D12  
 PH5/UART10\_nRTS/UART9\_TXD/LCM\_DATA13/LCM\_MPU\_D13  
 PH6/UART10\_RXD/LCM\_DATA14/LCM\_MPU\_D14  
 PH7/UART10\_TXD/LCM\_DATA15/LCM\_MPU\_D15  
 PC12/UART12\_nCTS/UART11\_RXD/LCM\_DATA16/LCM\_MPU\_D16  
 PC13/UART12\_nRTS/UART11\_TXD/LCM\_DATA17/LCM\_MPU\_D17  
 PC14/UART12\_RXD/LCM\_DATA18/LCM\_MPU\_CS  
 PC15/UART12\_TXD/LCM\_DATA19/LCM\_MPU\_TE/LCM\_MPU\_VSYNC  
 PH12/UART14\_nCTS/UART13\_RXD/LCM\_DATA20  
 PH13/UART14\_nRTS/UART13\_TXD/LCM\_DATA21  
 PH14/UART14\_RXD/LCM\_DATA22  
 PH15/UART14\_TXD/LCM\_DATA23

LCM/UART\_3,4,5,6,7,8,9,10,11,12,13,14

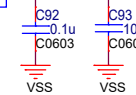
CAN\_0,3/I2C\_3/PWM\_1/SPI\_2/INT\_1,2,3/CLK0

MA35D16F9(7)87C

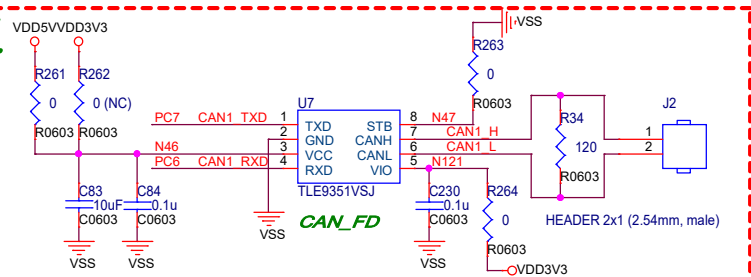
Note: The VDDIO4 is the input voltage of I/O group 4 and its voltage is 1.8V ~ 3.3V. This voltage should be matched with the voltage of external connected device. (The default is 3.3V on this board)

VDDIO4

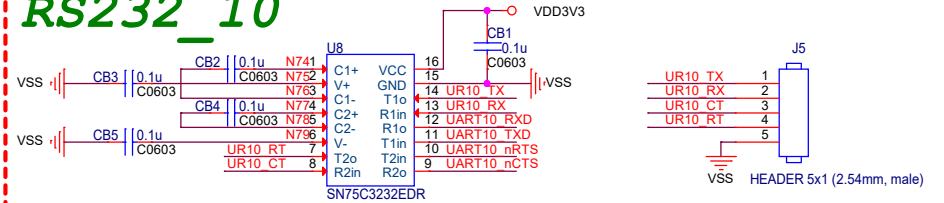
106VDDIO4 VDD3V3 L1P FB OVD3V3



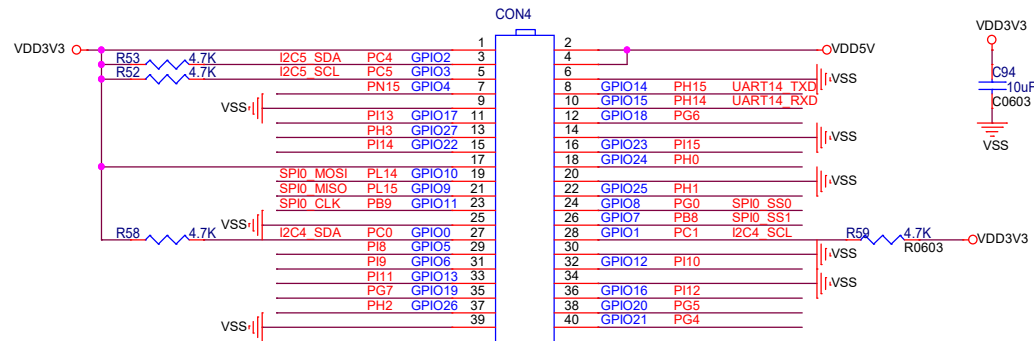
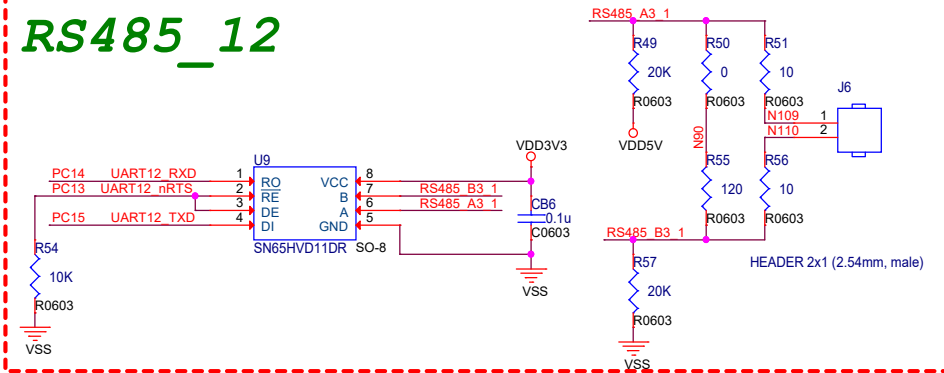
CAN\_1



RS232\_10

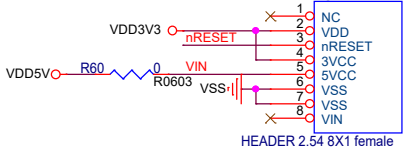
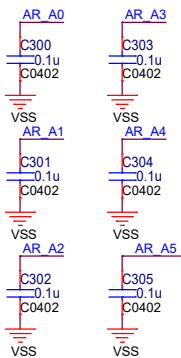
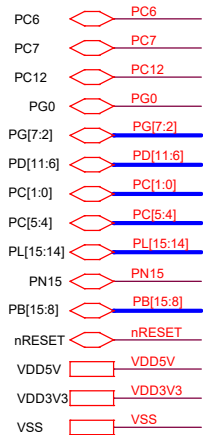


RS485\_12

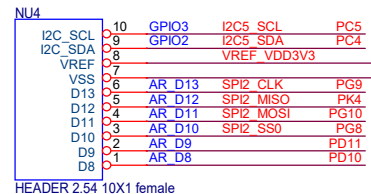
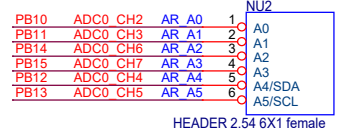


HEADER 20x2 (2.54mm, male)

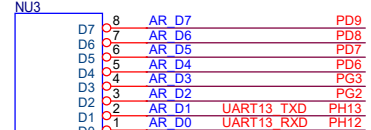
Raspberry Pi Connector



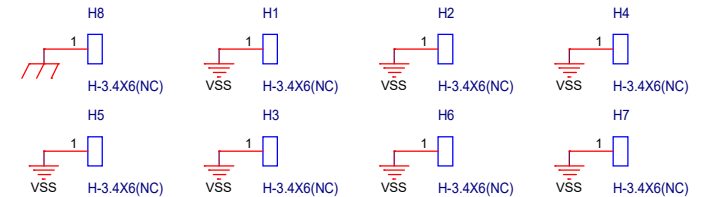
Arduino UNO Connector



HEADER 2.54 10x1 female



HEADER 2.54 8x1 female



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NuMaker-IoT-MA35D16F90 (LQFP216)

05. RP PI (VDDIO4)

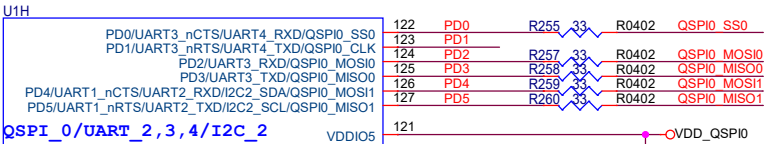
Wednesday, January 04, 2023

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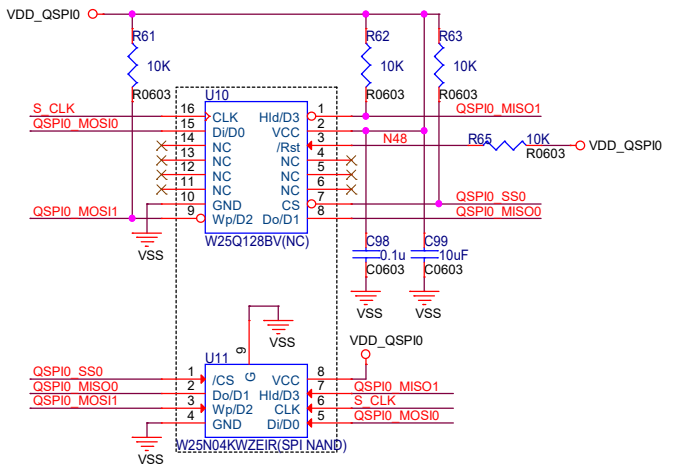
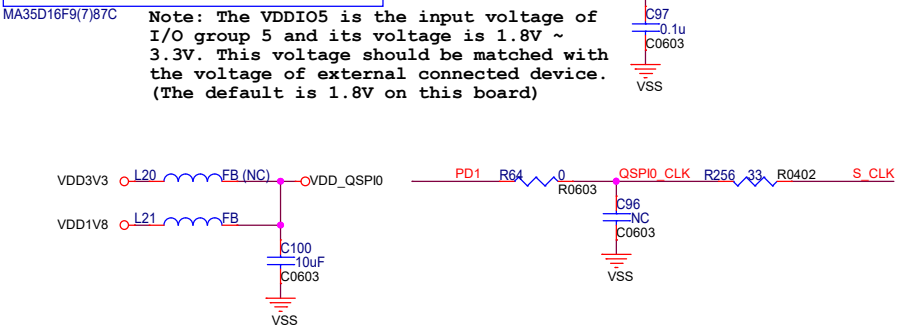
Rev V2.3



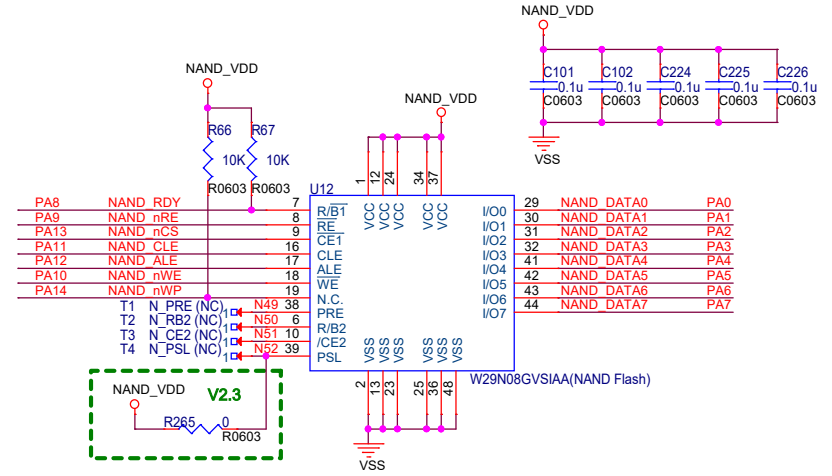
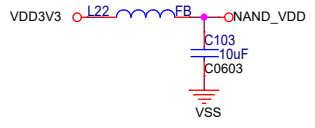
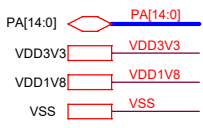
QSPI0\_Flash

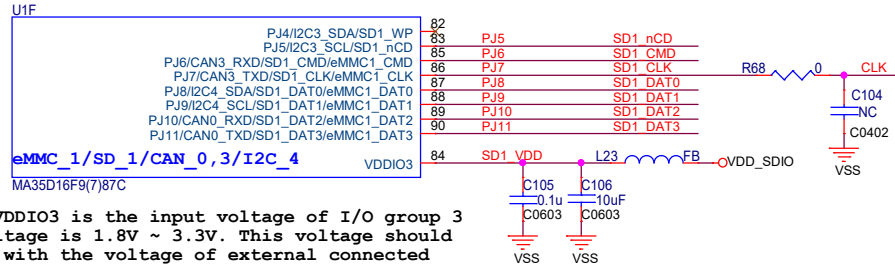


Note: The VDDIO5 is the input voltage of I/O group 5 and its voltage is 1.8V ~ 3.3V. This voltage should be matched with the voltage of external connected device. (The default is 1.8V on this board)

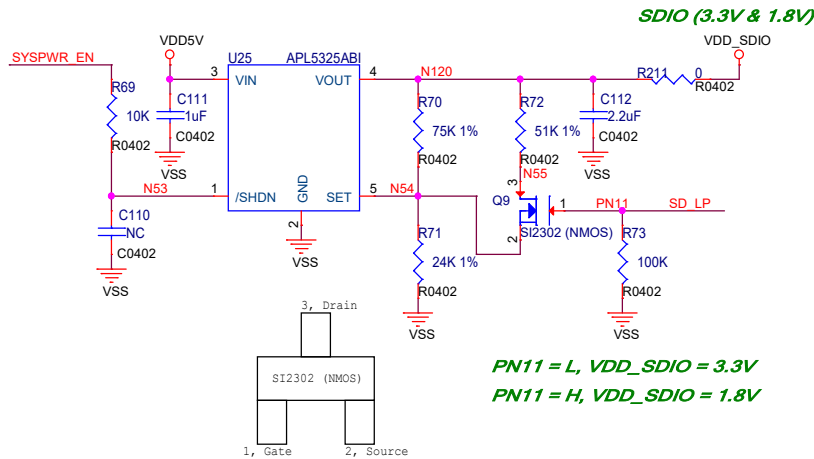


NAND\_Flash





Note: The VDDIO3 is the input voltage of I/O group 3 and its voltage is 1.8V ~ 3.3V. This voltage should be matched with the voltage of external connected device. (The default is 3.3V but can be controlled by GPIO PN11 high or low state that follows the SD3.0 timing on this board)

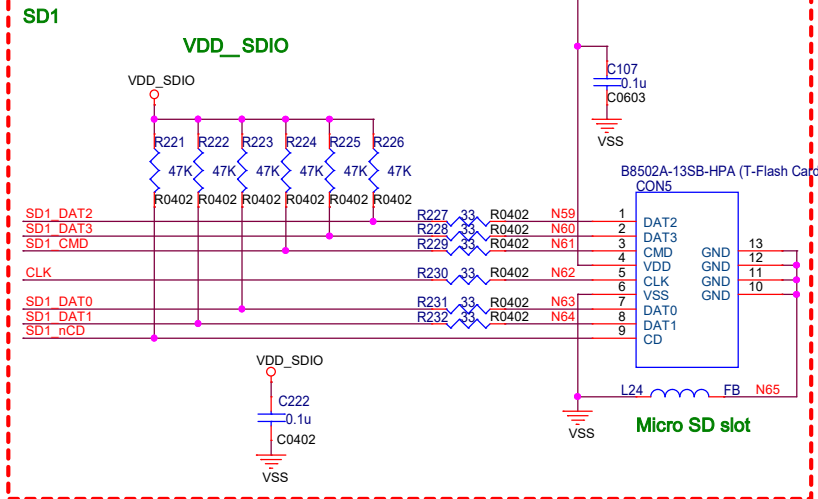
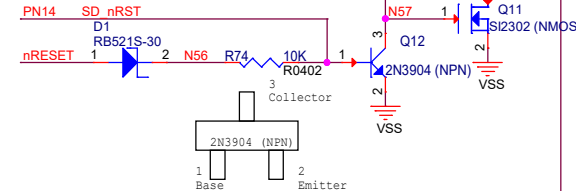


PN11 = L, VDD\_SDIO = 3.3V  
PN11 = H, VDD\_SDIO = 1.8V

For SD card compatibility

PN14 = L, VDD\_SD OFF  
PN14 = H, VDD\_SD ON

nRESET = L, VDD\_SD OFF  
nRESET = H, VDD\_SD ON



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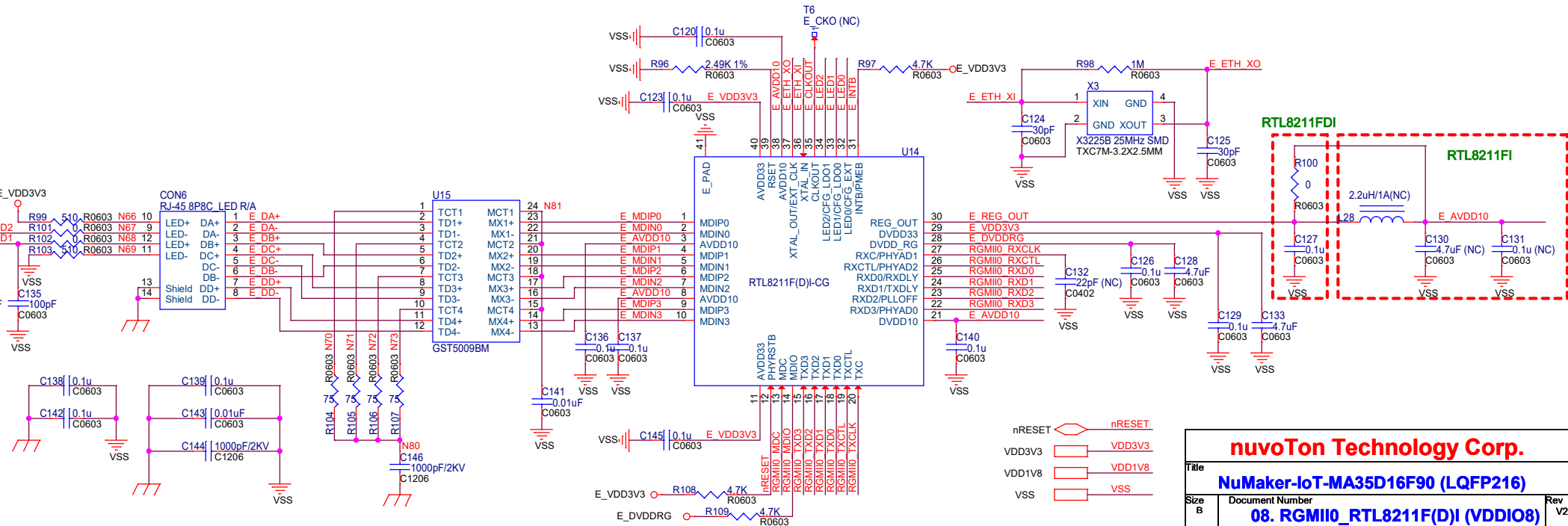
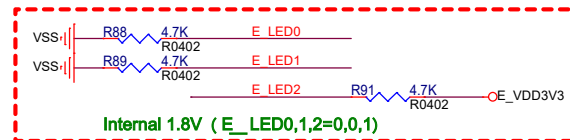
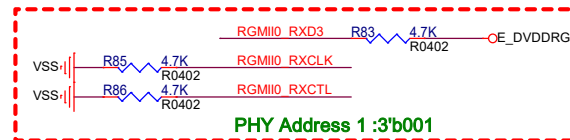
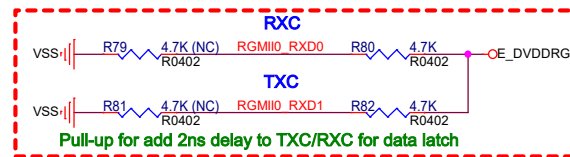
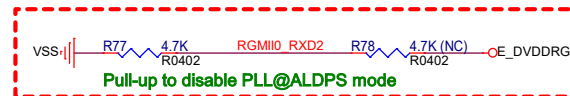
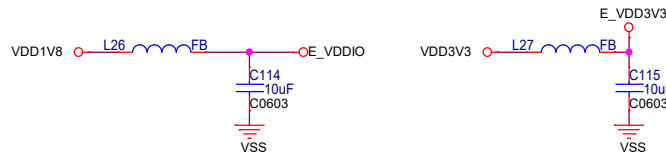
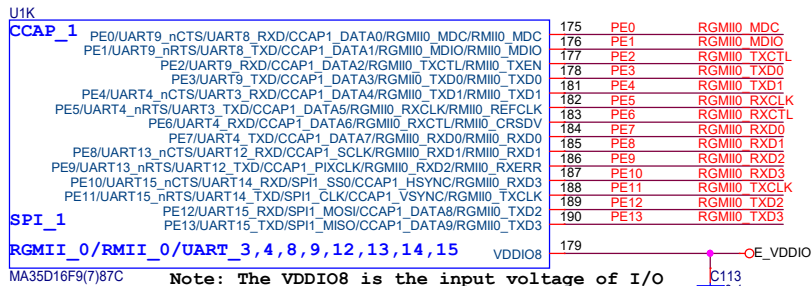
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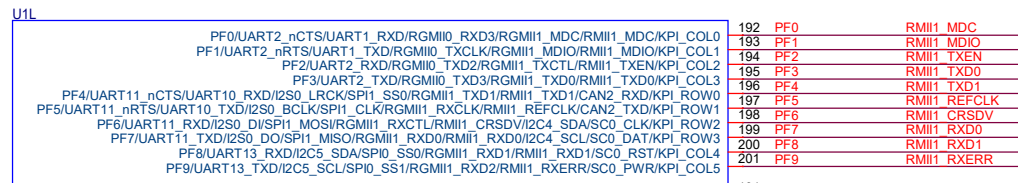
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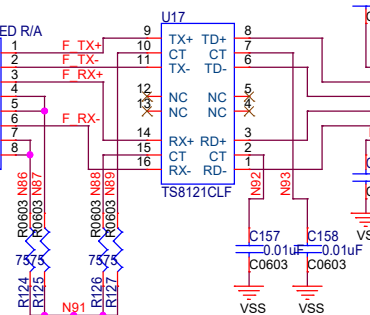
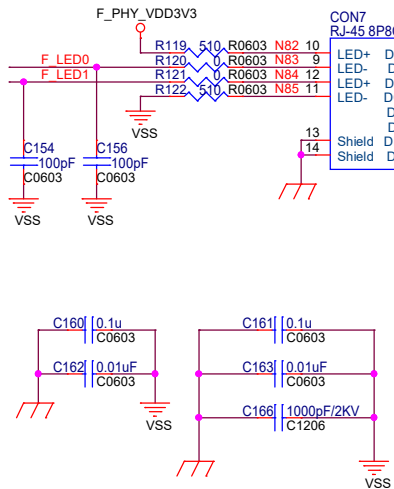
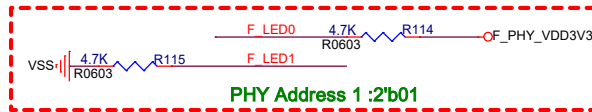
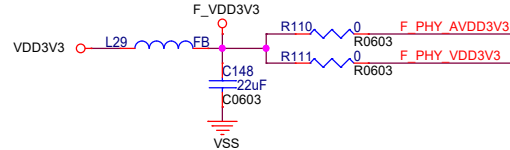
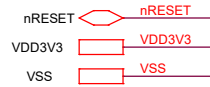
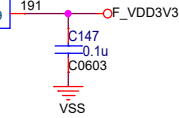
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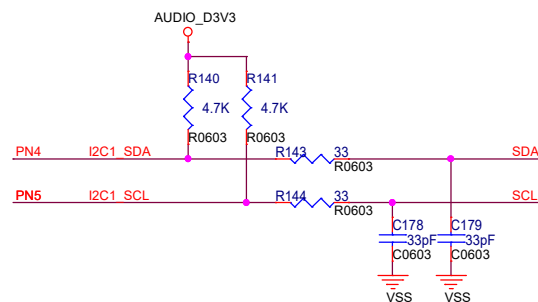
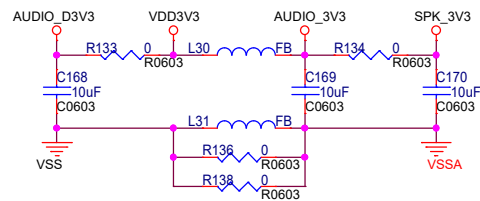




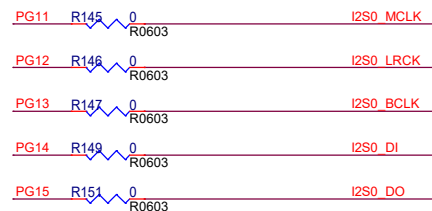


Note: The VDDIO9 is the input voltage of I/O group 9 and its voltage is 1.8V ~ 3.3V. This voltage should be matched with the voltage of external connected device. (The default is 3.3V on this board)

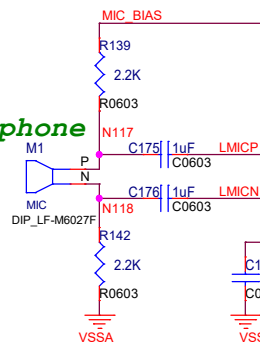




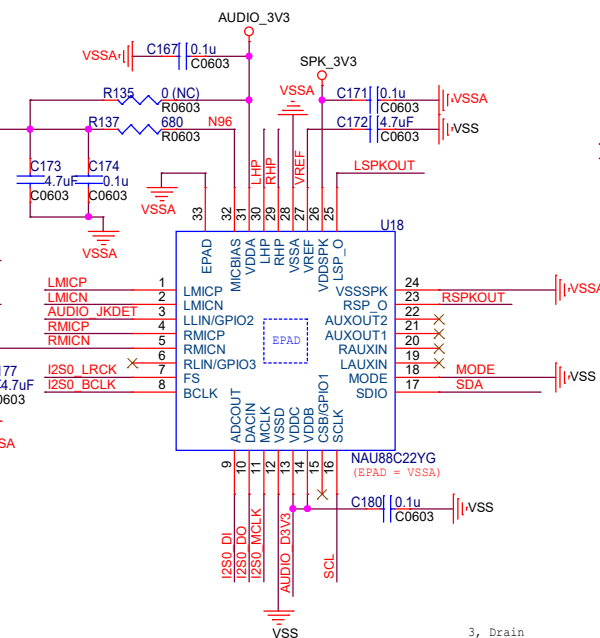
**PG11~15 Connect to I2S0 (SWJ)**



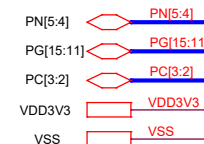
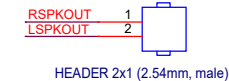
## Microphone



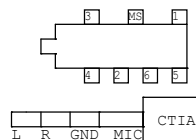
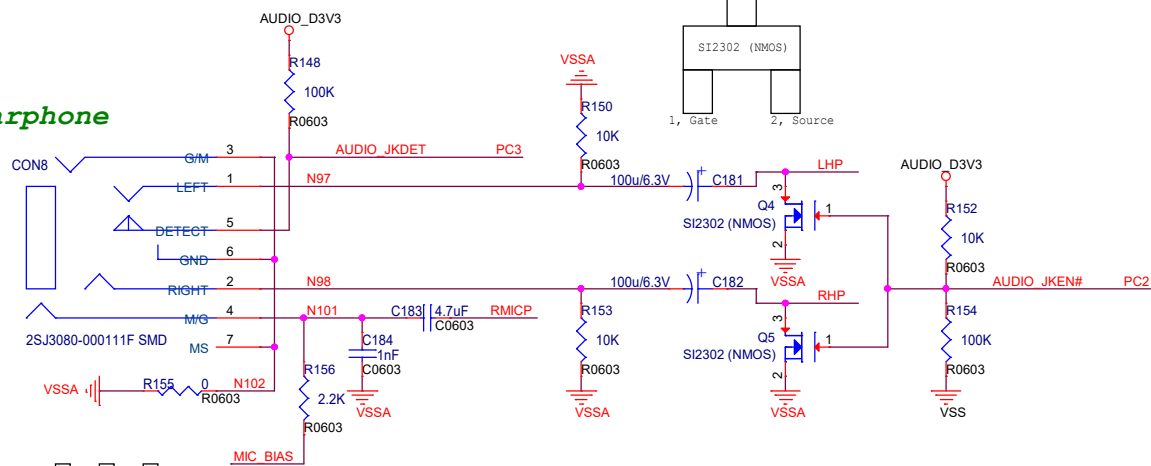
## Codec



## Speaker

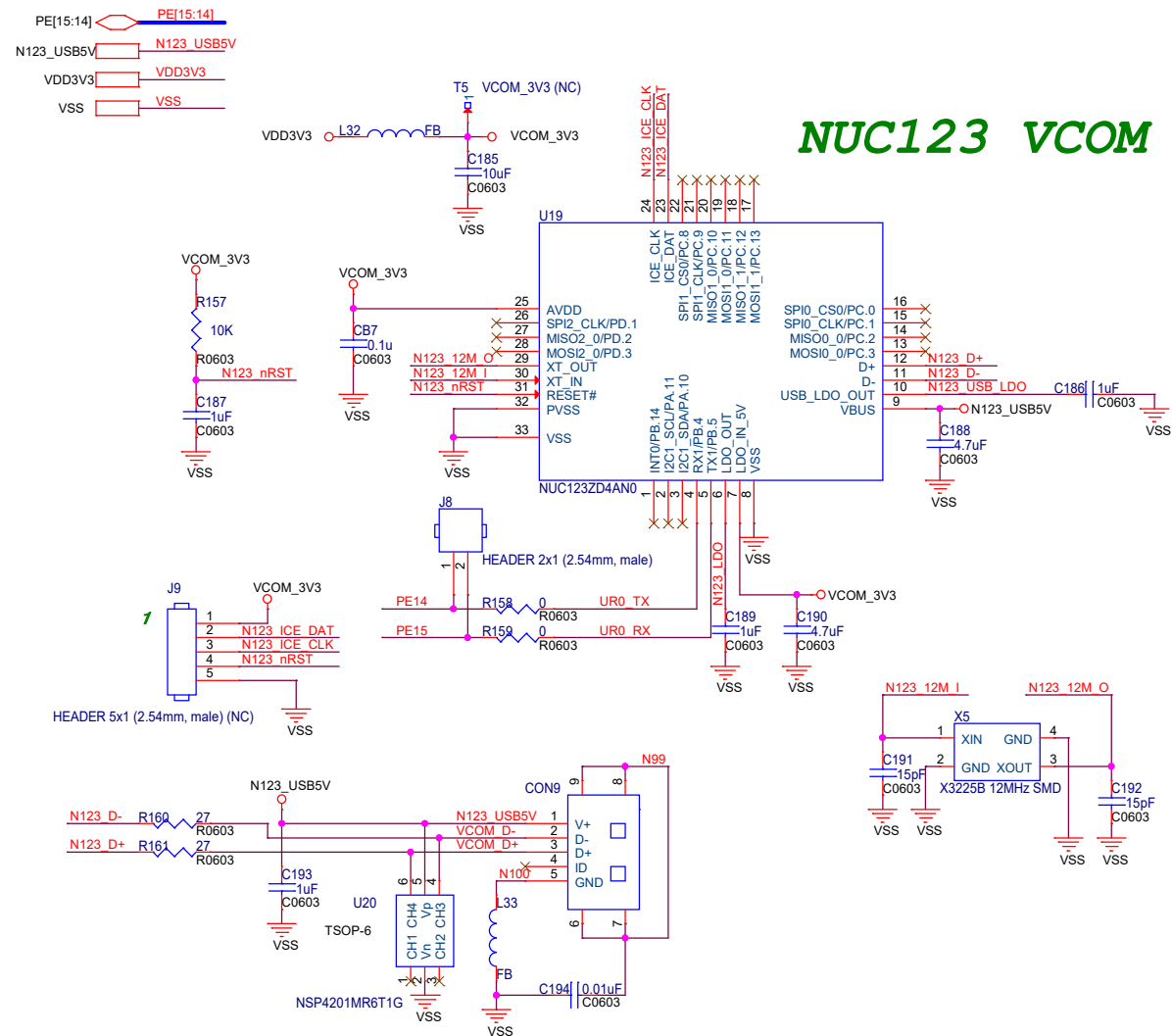


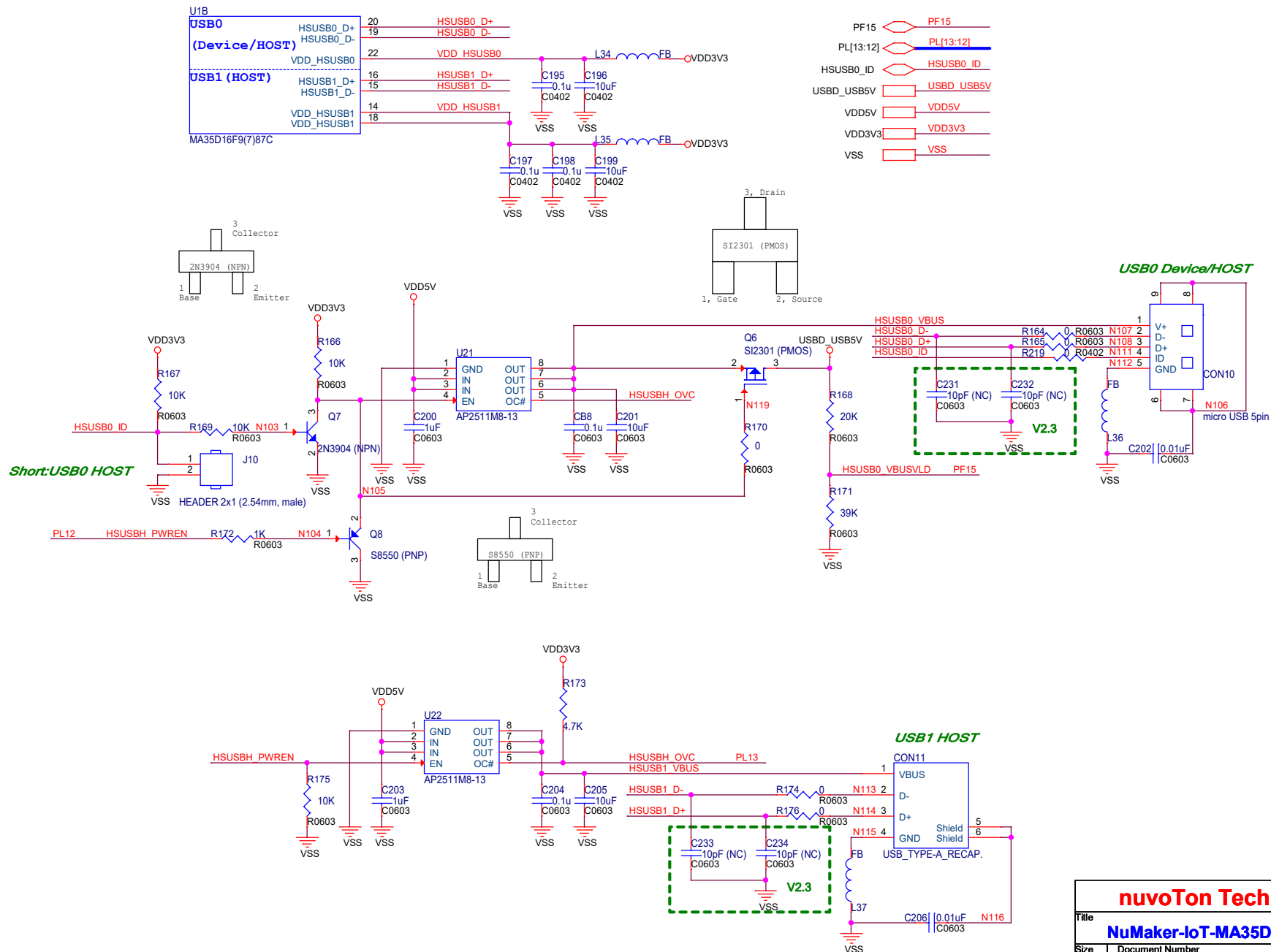
## Earphone



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